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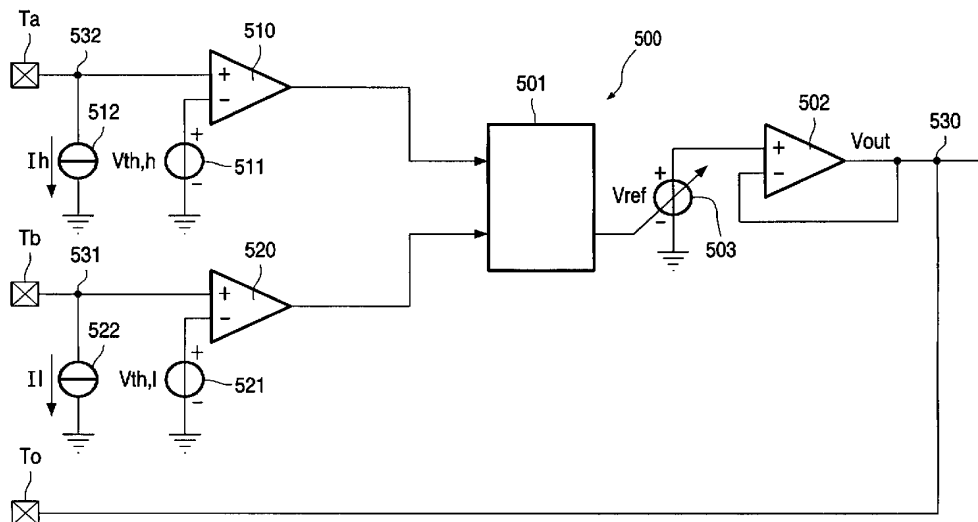
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(54) Title: VOLTAGE REGULATOR CIRCUIT ARRANGEMENT



(57) Abstract: The invention relates to a voltage regulator circuit arrangement comprising a voltage regulator for generating an output voltage in dependence of a reference signal, characterized in that a reference signal generation circuit is provided for generating said reference signal comprising a plurality of inputs connected to internal terminals, whereby a sub-set of said plurality internal terminals is connected to an external terminal.

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